

# Half Bridge IGBT Module

PDS5719-1.2 February 2004

## **FEATURES**

- Trench Gate Field Stop Technology
- Low Conduction Losses
- Low Switching Losses
- 10µs Short Circuit Withstand

## **APPLICATIONS**

- Motor Drives
- Wind Turbines
- UPS Systems

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 600V to 3300V and currents up to 3600A.

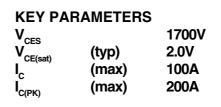
The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

### **ORDERING INFORMATION**

Order As:

#### DIM100WHS17-E000

Note: When ordering, please use the complete part number.



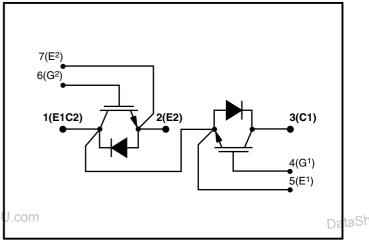
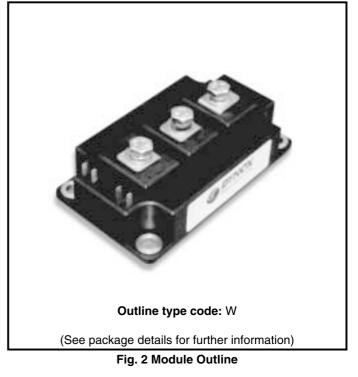


Fig. 1 Half bridge circuit diagram



Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures. DataShectwowdynexsemi.com



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### **ABSOLUTE MAXIMUM RATINGS - PER ARM**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

#### T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V	1700	V
$V_{ges}$	Gate-emitter voltage	-	±20	V
۱ <sub>с</sub>	Continuous collector current	$T_{case} = 80^{\circ}C$	100	A
I <sub>С(РК)</sub>	Peak collector current	1ms, T <sub>case</sub> = 110°C	200	A
P <sub>max</sub>	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	625	w
l²t	Diode I <sup>2</sup> t value	$V_{R} = 0, t_{p} = 10ms, T_{vj} = 125^{\circ}C$	1.4	kA²s
$V_{isol}$	Isolation voltage - per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4.0	kV
Q <sub>PD</sub>	Partial discharge - per module	IEC1287. V <sub>1</sub> = 1800V, V <sub>2</sub> = 1300V, 50Hz RMS	10	рС

# et4U.coTHERMAL AND MECHANICAL RATINGS

Internal insulation:  $Al_2O_3$ Baseplate material: Cu Creepage distance: 24mm DataSheet4U.com Clearance: 13mm CTI (Critical Tracking Index): 175

Symbol Parameter **Test Conditions** Min. Max. Units Тур. °C/W  ${\sf R}_{\rm th(j-c)}$ Thermal resistance - transistor (per arm) Continuous dissipation -0.2 junction to case  $\mathsf{R}_{\mathsf{th(j-c)}}$ °C/W Thermal resistance - diode (per arm) Continuous dissipation -0.36 junction to case °C/W  $\mathsf{R}_{\mathsf{th(c-h)}}$ Thermal resistance - case to heatsink Mounting torque 5Nm 0.15 \_ -(per module) (with mounting grease)  $T_i$ Junction temperature Transistor \_ \_ 150 °C Diode \_ \_ 125 °C -40 125 °C \_ Storage temperature range -T<sub>stg</sub> 3 5 Mounting - M6 Nm Screw torque -5 2.5 Nm Electrical connections - M6 -

### 2/8

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# **ELECTRICAL CHARACTERISTICS**

## $T_{case} = 25^{\circ}C$ unless stated otherwise.

	Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
	I <sub>CES</sub>	Collector cut-off current	$V_{\text{GE}} = 0V, V_{\text{CE}} = V_{\text{CES}}$	-	-	0.25	mA
			$V_{\text{GE}} = 0V, V_{\text{CE}} = V_{\text{CES}}, T_{\text{case}} = 125$	5°C -	-	1.25	mA
	I <sub>GES</sub>	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$	-	-	200	nA
	V <sub>GE(TH)</sub>	Gate threshold voltage	$I_c = 4mA$ , $V_{ge} = V_{ce}$	5.2	5.8	6.4	V
	V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 100A	-	2.0	2.4	V
			$V_{GE} = 15V, I_{C} = 100A, T_{Case} = 12$	25°C -	2.3	3.0	V
	I <sub>F</sub>	Diode forward current	DC	-	-	100	A
	I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms	-	-	200	Α
	V <sub>F</sub>	Diode forward voltage	I <sub>F</sub> = 100A	-	1.9	2.3	V
			I <sub>F</sub> = 100A, T <sub>case</sub> = 125°C	-	2.0	2.4	V
	C <sub>ies</sub>	Input capacitance	$V_{_{CE}} = 25V, V_{_{GE}} = 0V, f = 1MHz$	-	9.5	-	nF
4U.com	L <sub>M</sub>	Module inductance	taSheet4U.com	-	20	-	nHPa
	R <sub>INT</sub>	Internal transistor resistance - per arm	-	-	0.6	-	mΩ
	SC <sub>Data</sub>	Short circuit. I <sub>sc</sub>	$T_j = 125^{\circ}C, V_{cc} = 900V,$	I, -	438	-	A
			$t_p \leq 10 \mu s, V_{CE(max)} = V_{CES} - L^*. di/dt$	I <sub>2</sub> -	375	-	A
			IEC 60747-9				

Note:

 $L^*$  is the circuit inductance +  $L_M$ 

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# **ELECTRICAL CHARACTERISTICS**

#### T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>c</sub> = 100A	-	750	-	ns
t <sub>r</sub>	Fall time	$V_{ge} = \pm 15V$	-	200	-	ns
E <sub>OFF</sub>	Turn-off energy loss	$V_{CE} = 900V$	-	20	-	mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 15\Omega$	-	275	-	ns
t <sub>r</sub>	Rise time	L ~ 70nH	-	100	-	ns
E <sub>on</sub>	Turn-on energy loss		-	32	-	mJ
Q <sub>g</sub>	Gate charge		-	1.1	-	μC
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 100A, V <sub>R</sub> = 900V,	-	25	-	μC
I <sub>rr</sub>	Diode reverse current	$dI_{F}/dt = 1400A/\mu s$	-	110	-	А
E <sub>REC</sub>	Diode reverse recovery energy		-	10	-	mJ

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# T<sub>case</sub> = 125°C unless stated otherwise

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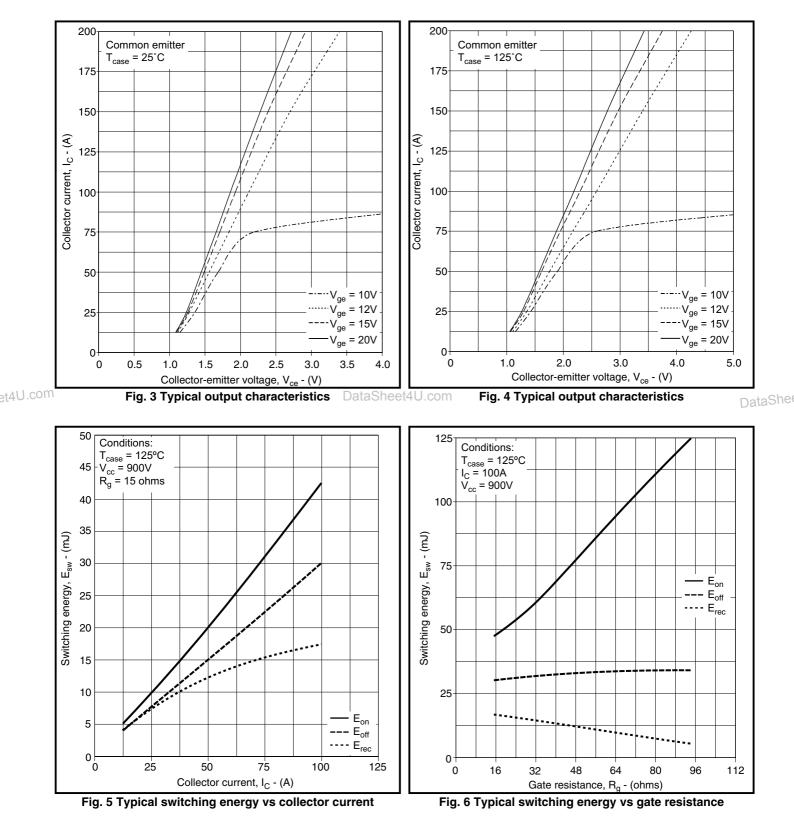
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 100A	-	850	-	ns
t <sub>r</sub>	Fall time	$V_{GE} = \pm 15V$	-	200	-	ns
E <sub>OFF</sub>	Turn-off energy loss	V <sub>CE</sub> = 900V	-	30	-	mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 15\Omega$	-	350	-	ns
t <sub>r</sub>	Rise time	L ~ 70nH	-	100	-	ns
E <sub>on</sub>	Turn-on energy loss		-	42	-	mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 100A, V <sub>R</sub> = 900V,	-	40	-	μC
I <sub>rr</sub>	Diode reverse current	dl <sub>F</sub> /dt = 1400A/µs	-	120	-	Α
E <sub>rec</sub>	Diode reverse recovery energy		-	17.5	-	mJ

#### Note:

Switching Characteristic measurements taken using standard driver circuit conditions.

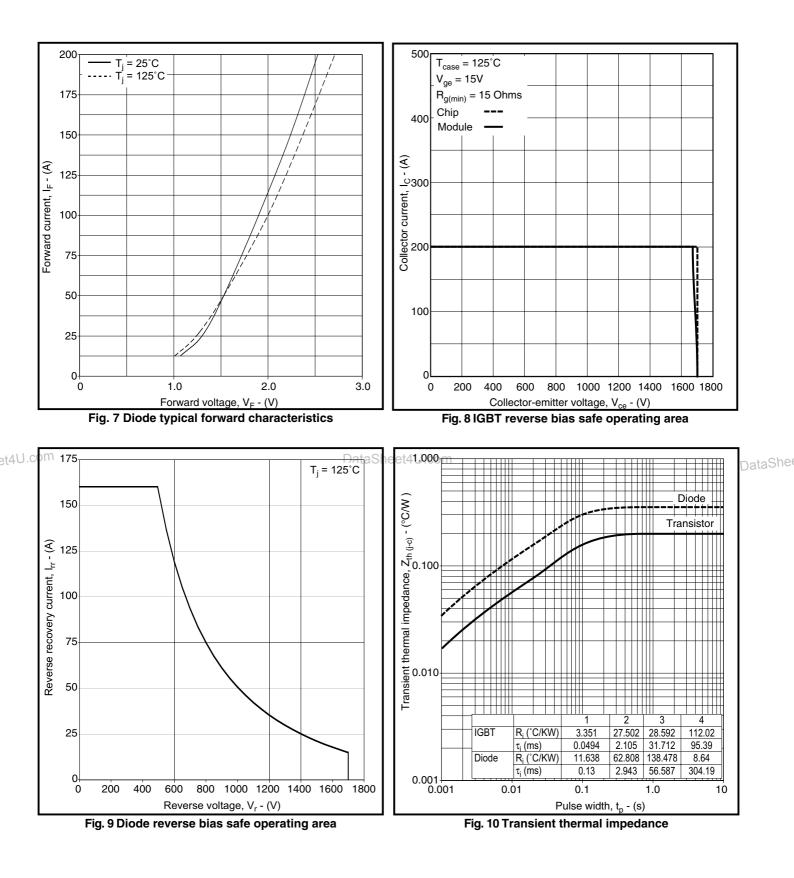


# **TYPICAL CHARACTERISTICS**



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6/8

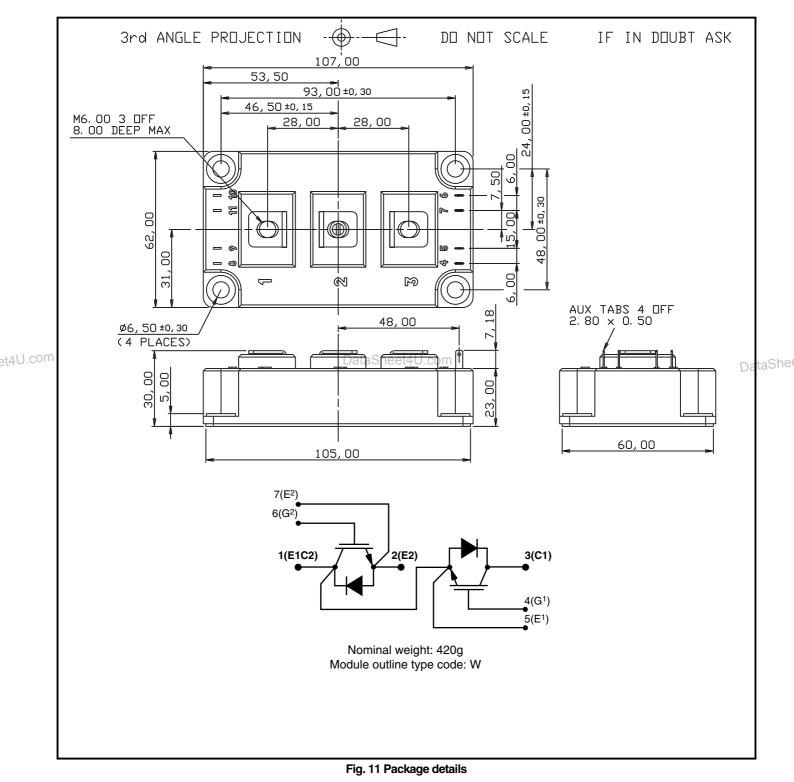
Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

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## **PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



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The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

#### **HEATSINKS**

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.

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